

Substitute for form 1449/PTO NINTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Complete if Known	
				Application Number	09/610,798
				Filing Date	July 6, 2000
				First Named Inventor	Suresh KRISHNA
				Art Unit	2164
				Examiner Name	Ortiz, Belix M.
Sheet	1	of	2	Attorney Docket Number	1875.4310003

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if Known)			
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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
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	FP2					
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	FP4					
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
	NPL1	Erich Nahum, David J. Yates, Sean O'Malley, Hilarie Orman, and Richard Schroepel; "Parallelized Network Security Protocols"; 1996 IEEE; pages 145-154.	
	NPL2	Surech et al, "Classification Engine in a Cryptography Acceleration Chip", U.S. Appl. No. 09/610,722 filed July 6, 2000, 43 pgs. (1875.4310002)	
	NPL3		
	NPL4		
	NPL5		
	NPL6		
	NPL7		
	NPL8		
	NPL9		
	NPL10		

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